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(11) **EP 0 666 686 B1**

(12) **EUROPEAN PATENT SPECIFICATION**

(45) Date of publication and mention  
of the grant of the patent:  
**28.04.1999 Bulletin 1999/17**

(51) Int Cl.<sup>6</sup>: **H04N 3/15**

(21) Application number: **95101477.8**

(22) Date of filing: **03.02.1995**

(54) **Color linear image sensor**

Zeilenförmiger Farbbildaufnahmesensor

Capteur linéaire d'images en couleur

(84) Designated Contracting States:  
**DE ES FR GB IT NL**

(30) Priority: **04.02.1994 JP 12764/94**

(43) Date of publication of application:  
**09.08.1995 Bulletin 1995/32**

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**EP-A- 0 388 658** **US-A- 4 280 141**

- **IEEE TRANSACTIONS ON CONSUMER ELECTRONICS**, vol. 38, no. 3, August 1992, **NEW-YORK (US)**, pages 576-582, XP000311896  
**T.C HSIEH ET AL.: "linear image sensor optimized for fast color scanning"**

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## Description

### BACKGROUND OF THE INVENTION

#### Field of the Invention

[0001] The present invention relates to an image reading apparatus for reading an image by a color image sensor and converting the image into an electrical signal.

#### Related Background Art

[0002] As a full-color reading system in a conventional apparatus of this type, ①light source switching system, ②prism separation system, ③color filter switching system, or ④on-chip color filter system is used. Of these systems, ④on-chip color filter system is considered to be optimal because of its high reading speed and color separation precision.

[0003] Figs. 1A to 1C are views showing the arrangement of a conventional color CCD linear image sensor of the on-chip color filter system.

[0004] A color CCD linear image sensor 1601 of this prior art is a three-line color CCD linear image sensor which has three CCD (Charge-Coupled Device) chips, e.g., CCD image sensors (chips) 1602 to 1604 with R, G, and B on-wafer color filters arranged in parallel on the same wafer, as shown in Figs. 1A to 1C.

[0005] Referring to Fig. 1A, a light-receiving unit 161 for performing photoelectric conversion in accordance with an incident light amount (reference numerals are added to only R, and G and B also have the same arrangement). On the CCD sensor elements of the light-receiving unit 161, a corresponding one of the R, G, and B color separation filters is arranged on the wafer. A light shield pixel unit is formed at the head portion of the light-receiving unit 161 so as to shield incident light by an aluminum mask arranged on the light-receiving unit 161 and always obtain an output in a dark state. Transfer gates 162 and 163 transfer charges accumulated in the light-receiving unit 161 to CCD shift registers 164 and 165 in accordance with a shift gate pulse  $\phi_{TG}$ . Charges accumulated in the even-numbered pixels of the light-receiving unit 161 are transferred to the CCD shift register 165 for the even-numbered pixels through the transfer gate 163. On the other hand, charges accumulated in the odd-numbered pixels of the light-receiving unit 161 are transferred to the CCD shift register 164 for the odd-numbered pixels through the transfer gate 162.

[0006] The CCD shift registers 164 and 165 perform CCD-transfer (perfect transfer) of the charges sent from the light-receiving unit 161 to output units and are driven by drive clocks  $\phi_1$  ( $\phi_{1R}$ ,  $\phi_{1FR}$ ,  $\phi_{1G}$ ,  $\phi_{1FG}$ ,  $\phi_{1B}$ , and  $\phi_{1FB}$ ) and  $\phi_2$  ( $\phi_{2R}$ ,  $\phi_{2FR}$ ,  $\phi_{2G}$ ,  $\phi_{2FG}$ ,  $\phi_{2B}$ , and  $\phi_{2FB}$ ) in two phases.

[0007] An output gate 166 sends the charges from the CCD shift registers 164 and 165 to output capacitor units

167a and 167b. The output capacitor units 167a and 167b convert the transferred charges into a voltage. Two source follower amplifiers 168a and 168b reduce the output impedance, thereby preventing noise from mixing in an output signal.

[0008] The output capacitor units 167a and 167b and the source follower amplifiers 168a and 168b constitute an FDA (Floating Diffusion Amplifier).

[0009] The color image sensor includes signal output terminals OSAR, OSBR, OSAG, OSBG, OSAB, and OSBB, reset pulse terminals  $\phi_{RAR}$ ,  $\phi_{RBR}$ ,  $\phi_{RAG}$ ,  $\phi_{RBG}$ ,  $\phi_{RAB}$ , and  $\phi_{RBB}$ , CCD shift register clock terminals  $\phi_{1R}$ ,  $\phi_{1G}$ ,  $\phi_{1B}$ ,  $\phi_{2R}$ ,  $\phi_{2G}$ , and  $\phi_{2B}$ , transfer gate clock terminals  $\phi_{TGR}$ ,  $\phi_{TGG}$ , and  $\phi_{TGB}$ , and source follower amplifier drain terminals ODR, ODG, and ODB.

[0010] In the color image sensor 1601 having the above arrangement, light incident on the light-receiving unit 161 is converted into charges proportional to the light amount. The charges of the even- and odd-numbered pixels are respectively transferred to the CCD shift registers 165 and 164 in accordance with the shift gate pulse  $\phi_{TG}$ . Thereafter, charges are output bit by bit to the FDA via the output gate 166 in accordance with the drive clocks  $\phi_1$  and  $\phi_2$ . The charge output is converted into a voltage by the output capacitor units 167a and 167b of the FDA and output through the two source follower amplifiers 168a and 168b and the output terminals OSA and OSB.

[0011] In this prior art, however, spectral characteristics as shown in Fig. 5 are obtained from the spectral transmissions of the R, G, and B color filters shown in Fig. 2, the spectral energy distribution of the light source shown in Fig. 3, and the spectral transmission of the infrared absorbing filter shown in Fig. 4. Charge amounts generated from the photodiodes of the CCDs 1602 to 1604 hold a relation  $B\text{-CCD} < R\text{-CCD} < G\text{-CCD}$ . The sensitivities of the CCDs 1602 to 1604 similarly satisfy  $B\text{-CCD} < R\text{-CCD} < G\text{-CCD}$ . For example, the sensitivities of the R, G, and B CCDs are  $R : 2.1 \text{ V/1x.sec}$ ,  $G : 2.6 \text{ V/1x.sec}$ , and  $B : 0.86 \text{ V/1x.sec}$ , respectively.

[0012] The saturation output voltages of the CCDs 1602 to 1604 are equal to each other because the CCD registers 164 and 165 normally have the same size.

[0013] When this three-line color CCD linear sensor is to be actually used, the arrangement of the reading system is determined depending on the light amount necessary for obtaining an output voltage for a desired S/N ratio.

[0014] That is, the arrangement is determined by the light amount with which the B-CCD 1604 having the lowest sensitivity outputs a voltage necessary for obtaining a desired S/N ratio. If the desired S/N ratio is 48 dB (256 levels), and the noise level from the CCD is 1 mV, the minimum output voltage is 256 mV.

[0015] When the output voltage from the B-CCD is 256 mV, the output voltages from the R-CCD 1602 and the G-CCD 1603 are respectively given as follows:

$$2.1(V/1x.sec)/0.86(V/1x.sec) \times 256 \text{ mV} < 625 \text{ mV},$$

and

$$2.6(V/1x.sec)/0.86(V/1x.sec) \times 256 \text{ mV} < 774 \text{ mV}$$

[0016] Assume that the above output voltages are obtained for a certain accumulation time  $T_1$  ( $\mu\text{sec}$ ), and the reading speed of this image reading apparatus is to be increased. In this case, if the reading speed is to be doubled, an accumulation time  $T_2$  is represented by  $T_2 = T_1/2$ . The output voltages from the CCDs are B-CCD : 128 mV, R-CCD : 325 mV, and G-CCD : 383 mV. Therefore, an S/N ratio of 48 dB for the B-CCD 1604 cannot be obtained.

[0017] To prevent this, the light amount must be doubled. However, the light amount cannot simply be doubled because of the problem of an increase in temperature of the apparatus. To constitute the apparatus, the illumination light amount must be increased while preventing the problem of an increase in temperature. If the light amount cannot be sufficiently increased, the S/N ratio of the B-CCD must be decreased, resulting in degradation in image quality.

[0018] Furthermore, in document US-A-4 280 141, there is disclosed an imaging system which operates in the time delay and integration mode (TDI mode). This system comprises a multi-channel matrix array of CCD devices wherein a plurality of sensor cells in each channel are subdivided and operated in discrete intercoupled subarrays with a read-out CCD shift register terminating each end of the channels. Clock voltages are applied to the subarrays and are manipulated to selectively cause charge signal flow in each subarray in either direction independent of the other subarrays. Thus, a form of selective electronic exposure control is created, which provides an effective variable time delay and variable integration stages.

[0019] Moreover, from document IEEE Transactions on Consumer Electronics, vol. 38, no. 3, August 1992, pages 576-582; T.C. Hsieh et al.: "linear image sensor optimized for fast color scanning", there is known a scanning system constituted by a CCD-sensor. The sensor consists of three independent CCD imagers, one for each of three colors, integrated on a single die. Each is an area CCD comprising an array of 2055 by 17 pixels. The three imagers are precisely aligned and displaced by 28 pixels from each other in the TDI direction. The individual CCD's are operated in TDI mode to give an optical sensitivity increase of approximately one order of magnitude over a conventional linear array of the same resolution. The imager for blue has 16 TDI rows, the imager for green has 5 TDI rows, and the imager for red has 6 TDI rows.

## SUMMARY OF THE INVENTION

[0020] It is an object of the present invention to provide a linear image sensor capable of reading an image with high quality at a sufficient signal level.

[0021] According to the present invention, this object is achieved by a linear image sensor as defined in the claims 1 to 6.

[0022] Further objects, features and advantages of the present invention will become apparent from the following description taken in conjunction with the drawings.

## BRIEF DESCRIPTION OF THE DRAWINGS

[0023]

Fig. 1 is comprised of Figs. 1A to 1C showing views illustrating the arrangement of a conventional color linear image sensor;

Fig. 2 is a graph showing the spectral transmission characteristics of the color filters of the color linear image sensor;

Fig. 3 is a graph showing the spectral energy distribution characteristics of a light source;

Fig. 4 is a graph showing the spectral transmission characteristics of an infrared absorbing filter;

Fig. 5 is a graph showing the overall spectral characteristics of the color linear image sensor;

Fig. 6 is a view showing the arrangement of a color linear image sensor;

Fig. 7 is a sectional view showing the arrangement of an image reading apparatus of the present invention;

Fig. 8 is a view showing the arrangement of a color linear image sensor according to the first embodiment of the present invention;

Figs. 9A and 9B are timing charts of the color linear image sensor according to the embodiment of the present invention; and

Fig. 10 is a view showing the arrangement of a color linear image sensor according to the second embodiment of the present invention.

## DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0024] Fig. 6 is a view showing the arrangement of a linear image sensor of the Time Delay and Integration (TDI) system according to an embodiment of the present invention. In the TDI system, a plurality of lines of photoelectric conversion means are arranged in the linear image sensor. Output signals from the plurality of lines of photoelectric conversion means are sequentially synthesized with each other in synchronism with the reading speed of a scanner having the line sensor. With this operation, an output signal multiplied by the number of lines of the photoelectric conversion means of the line

sensor can be obtained.

[0025] Referring to Fig. 6, a color CCD linear sensor 1700 allows the above-described TDI operation and has R, B, and G CCD linear sensor units 1701 to 1703.

[0026] Linear photodiode arrays 1704a to 1704c serve as photoelectric conversion means having R on-chip color filters. CCD shift registers 1705a and 1705b horizontally transfer charges generated in the linear photodiode arrays 1704a to 1704c to output units 1718a and 1718b. The two CCD shift registers 1705a and 1705b are arranged to improve the reading speed of the color CCD linear image sensor.

[0027] Similarly, linear photodiode arrays 1706a to 1706c and 1708a to 1708c have B and G on-chip color filters, respectively. The B CCD linear sensor unit 1702 includes B CCD shift registers 1707a and 1707b and B output units 1719a and 1719b, and the G CCD linear sensor unit 1703 includes G CCD shift registers 1709a and 1709b and G output units 1720a and 1720b.

[0028] Shift gates SH1 (1710) and SH3 (1712) temporarily accumulate the charges generated in the linear photodiode arrays in synchronism with the reading speed of the scanner. Shift gates SH2 (1711) and SH4 (1713) transfer the charges from the shift gates SH1 (1710) and SH3 (1712) to the linear photodiode arrays 1704b and 1704c, thereby synthesizing the charges accumulated in the shift gates SH1 (1710) and SH3 (1712) with charges generated in the next linear photodiode arrays 1704b and 1704c.

[0029] A shift gate SH5 (1714) transfers the charges generated in the linear photodiode 1704c to the CCD shift registers 1705a and 1705b in synchronism with the reading speed of the scanner. The charges are transferred from the linear photodiode array 1704c through the shift gates SH5 (1714), and charges for one pixel corresponding to shift gates SG1 (1715) and SG2 (1716) are sequentially transferred to the CCD shift registers 1705b and 1705a through the shift gates SG1 and SG2.

[0030] A transfer gate TG1 (1717) transfers the charges between the CCD shift registers 1705a and 1705b.

[0031] The B and G CCD linear sensor units 1702 and 1703 have the same arrangement as that of the R CCD linear sensor unit 1701, and a detailed description thereof will be omitted.

[0032] In the color CCD linear sensor 1700, integration of the charges and transfer of the charges between the CCDs and the shift registers are performed in only directions indicated by arrows, as described above.

[0033] Fig. 7 is a sectional view showing the arrangement of a scanner 1800 having the color CCD linear sensor 1700 in Fig. 6.

[0034] The scanner 1800 has a scanner main body 1800a, a document feeder 1800b, and a control unit 1800c.

[0035] An original is mounted on a platen glass 1810. A mirror unit 1812 is constituted by a halogen lamp 1805 for exposing the original and a first reflecting mirror

1802.

[0036] A second reflecting mirror 1803 and a third reflecting mirror 1804 constitute a mirror unit 1813.

[0037] A lens unit 1801 reduces and focuses a reflected optical image from the original which is exposed and scanned by the halogen lamp 1805 on the color CCD linear sensor 1700. A read/feed platen glass 1809 is used when the original is to be fed/read using the document feeder 1800b.

[0038] If the mirror units 1812 and 1813 are moved by a stepping motor 1814 in a direction (sub-scanning direction) indicated by an arrow A at a scanning speed of 2:1 to read the original mounted on the platen glass 1810, the mirror units 1812 and 1813 start from positions indicated by broken lines.

[0039] The document feeder 1800b has the following constituent elements.

[0040] The document feeder 1800b has an original input tray 1806, an original pick-up roller 1807, feed rollers 1808 for feeding the original, and a discharge tray 1811.

[0041] In this case, an original is placed on the input tray 1806 with its surface facing up. In single-side original reading, the original is sent to the feed rollers by the pick-up roller 1807, fed by the feed rollers in accordance with the original reading timing, and conveyed in a direction indicated by a dotted arrow. When the original passes on the platen glass, a reflected optical image is reduced and focused on the color CCD linear sensor 1700 through the mirror units 1812 and 1813 and the lens unit 1801.

[0042] In two-side original reading, the original fed by the feed rollers is conveyed in a direction indicated by a solid arrow. First of all, the upper surface of the original passes the reading position of the feed/read platen glass and is read. Thereafter, the original is reverted along the conveyance route. The lower surface is read in a direction opposite to the reading direction in upper surface reading, and the original is discharged onto the discharge tray 1811 as in single-side original reading. The above series of operations are performed under control of the control unit 1800c.

[0043] At this time, the scanning direction of the image focused on the color CCD linear image sensor is a direction indicated by an arrow B in upper surface reading, or a direction indicated by an arrow C in lower surface reading. In the color CCD linear sensor 1700 in Fig. 6, integration for the TDI is performed in only one direction. For this reason, depending on the setting direction of the color CCD linear sensor 1700, only one of the upper and lower surfaces is read.

[0044] To solve this problem, a linear image sensor as shown in Fig. 8 has been proposed.

[0045] A color CCD linear image sensor 100 of this embodiment is constituted by R, B, and G CCD linear sensor units 101 to 103.

[0046] Each of linear photodiode arrays 102a to 102c has an R on-chip color filter. Each of linear photodiode arrays 103a to 103c and 104a to 104c has a B or G on-

chip color filter.

**[0047]** CCD shift registers 105a and 105b and 108a and 108b horizontally transfer charges generated in the linear photodiode arrays 102a to 102c to output units 109a and 109b and 130a and 130b, respectively. The CCD shift registers 105a and 105b are used in forward-direction (upper surface) reading (direction indicated by a solid arrow in Fig. 8), and the CCD shift registers 108a and 108b are used in backward-direction (lower surface) reading (direction indicated by a dotted arrow in Fig. 8).

**[0048]** The CCD shift registers 105a and 105b also serve as CCD shift registers (for B backward-direction reading) for horizontally transferring charges generated in the linear photodiode arrays 103a to 103c to the output units 109a and 109b.

**[0049]** CCD shift registers 106a and 106b transfer the charges generated in the B linear photodiode arrays 103a to 103c to output units 110a and 110b and are used in B forward-direction reading.

**[0050]** The CCD shift registers 106a and 106b also serve as CCD shift registers (for G backward-direction reading) for outputting the charges generated in the G linear photodiode arrays 104a to 104c.

**[0051]** CCD shift registers 107a and 107b horizontally transfer the charges generated in the G linear photodiode arrays 104a to 104c to output units 111a and 111b.

**[0052]** Shift gates SH1 (112), SH2 (113), and SH3 (114) transfer the charges generated in the linear photodiode array 102a to the next linear photodiode array 102b, and synthesize the charges with those generated in the linear photodiode array 102b. In forward-direction reading, the shift gates SH1 (112), SH2 (113), and SH3 (114) sequentially operate to transfer the charges in a direction indicated by a solid arrow. In backward-direction reading, the shift gates SH1 to SH3 (112 to 114) operate in an order reverse to that in forward-direction reading.

**[0053]** That is, the shift gates operate in an order of SH3 (114) → SH2 (113) → SH1 (112). Shift gates SH4 (115), SH5 (116), and SH6 (117) transfer the charges in the vertical direction to synthesize the charges generated in the photodiode arrays 102b and 102c. The orders of operations in forward-direction reading and backward-direction reading are SH4 → SH5 → SH6 and SH6 → SH5 → SH4, respectively.

**[0054]** A shift gate SH7 (118) transfers the charges generated in the linear photodiode array 102c to the horizontal CCD shift registers 105a and 105b in synchronism with the reading speed of the scanner. Switch gates SG1 (119) and SG2 (120) sequentially transfer the charges in the linear photodiode array 102c, which are transferred by the shift gate SH7 (118), to the horizontal CCD shift registers 105a and 105b in correspondence with each pixel. The charges of the odd-numbered pixels are transferred to the horizontal CCD shift register 105b by the switch gate SG1 (119), and those of the even-numbered pixels are transferred to the horizontal

CCD shift register 105a by the switch gate SG2 (120).

**[0055]** Transfer gates TG1 (121), TG2 (122), and TG3 (123) transfer the charges between the horizontal CCD shift registers 105a and 105b. As in the above-described transfer of the charges between the linear photodiodes, the order of the operations can be changed such as TG1 → TG2 → TG3 in forward-direction reading and TG3 → TG2 → TG1 in backward-direction reading. In this manner, the transfer direction can be switched between the forward direction and the backward direction (forward direction: solid arrow, backward direction: dotted arrow).

**[0056]** The horizontal CCD shift registers 105a and 105b are driven in two phases. As is normally known, two registers  $\phi 1$  and  $\phi 2$  are alternately arranged. When pulses are alternately input to the two registers, the potentials of the CCD registers change to sequentially transfer the charges in the directions of output units (109a and 109b). The above-described transfer of charges between the registers through the transfer gates TG1 to TG3 is performed using the register  $\phi 1$  of the two registers.

**[0057]** A shift gate SH8 (124) transfers the charges in the linear photodiode array 102a to the CCD shift registers 108a and 108b in synchronism with the reading speed of the scanner. Switch gates SG1 (125) and SG2 (126) sequentially transfer the charge in the shift gate SH8 to the CCD shift registers 108a and 108b in correspondence with each pixel. The charges of the odd-numbered pixels are transferred to the CCD shift register 108a by the switch gate SG1 (125), and those of the even-numbered pixels are transferred to the CCD shift register 108b by the switch gate SG2 (126).

**[0058]** Transfer gates TG1 (127), TG2 (128), and TG3 (129) transfer the charges between the CCD shift registers 108a and 108b. The operations are the same as those described above.

**[0059]** Figs. 9A and 9B are timing charts of the color CCD linear image sensor 100 in Fig. 8.

**[0060]** In forward-direction reading shown in Fig. 9A, the shift gate SH7 and the switch gates SG1 and SG2 go high (to be abbreviated as "H" hereinafter) at timing  $T_1$ . The charges in the linear photodiode array 102c are transferred to the switch gates SG1 (119) and SG2 (120). At  $T_2$ , the switch gate SG1 at "H" goes low (to be abbreviated as "L" hereinafter), and the register  $\phi 1$  at "L" goes "H", thereby transferring the charges in the switch gate SG1 (119) to the register  $\phi 1$  of the CCD shift register 105a.

**[0061]** At  $T_3$ , the register  $\phi 1$  at "H" goes "L", and the transfer gate TG1 goes "H", thereby transferring the charges from the register  $\phi 1$  to the transfer gate TG1 (121).

**[0062]** Similarly, at  $T_4$ , the transfer gate TG1 at "H" goes "L", and the transfer gate TG2 at "L" goes "H". At  $T_5$ , the transfer gate TG2 at "H" goes "L", and the transfer gate TG3 at "L" goes "H", thereby sequentially transferring the charges through the transfer gates TG1

(121), TG2 (122), and TG3 (123).

**[0063]** At  $T_6$ , the switch gate SG2 (120) at "H" goes "L", and the register  $\phi 1$  at "L" goes "H" again, thereby transferring the charges in the switch gate SG2 (120) to the register  $\phi 1$  of the CCD shift register 105a. At the same time, the transfer gate TG3 (123) at "H" goes "L", thereby transferring the charges in the transfer gate TG3 (123) to the register  $\phi 1$  of the CCD shift register 105b.

**[0064]** As shown in Fig. 9A, in transfer between the shift gates SH1 to SH7 related to the TDI operation, the charges are sequentially transferred one shift gate in one period. During the seventh period, the charges are transferred to the CCD shift registers 105a and 105b and read as an output.

**[0065]** In backward-direction transfer shown in Fig. 9B, the shift gate SH8 (124) operates in place of the shift gate SH7 (118), and the operation timings of the shift gates SH1 to SH6 (112 to 117) and the transfer gates TG1 to TG3 (121 to 123) are reversed to that in forward-direction transfer shown in Fig. 9A. Except for that, the operation is the same as that in forward-direction transfer.

**[0066]** The R CCD linear image sensor unit 101 of the color CCD linear image sensor 100 has been described above. In the B and G CCD linear image sensor units 102 and 103, the operations of constituent elements with the same reference symbols are the same, and a detailed description thereof will be omitted.

**[0067]** As described above, in this embodiment, an output signal from the photoelectric conversion means of the linear image sensor can be transferred in both the forward and backward directions. With this arrangement, degradation in image quality, which is caused by a decrease in S/N ratio due to a low output signal level, can be prevented, thereby obtaining an image with high quality.

**[0068]** In addition, integration of the output signal can be performed in both the forward and backward directions. For this reason, when both the upper and lower surfaces of an original or the like are to be read using a document feeder, reading is facilitated although the scanning direction changes in upper surface reading and lower surface reading.

**[0069]** Furthermore, when the upper and lower surfaces of a plurality of originals are to be continuously read, reading can be performed in a short period of time.

**[0070]** Fig. 10 is a view showing the arrangement of a color CCD linear image sensor of an image reading apparatus according to the second embodiment of the present invention.

**[0071]** The same reference numerals or symbols as in Fig. 8 denote the same constituent elements in Fig. 10, and a detailed description thereof will be omitted.

**[0072]** A color CCD linear image sensor 300 used in the present invention is constituted by R, B, and G CCD linear sensor units 301 to 303.

**[0073]** Horizontal shift registers 304a to 304d transfer charges generated in R linear photodiode arrays 102a

to 102c to output units 309a to 309d, respectively, in forward-direction reading. The horizontal CCD shift registers 304a to 304d transfer charges generated in B linear photodiode arrays 103a to 103c to the output units 309a to 309d, respectively, in backward-direction reading.

**[0074]** Similarly, horizontal CCD shift registers 305a to 305d transfer the charges generated in the B linear photodiode arrays 103a to 103c to output units 310a to 310d in forward-direction reading or the charges generated in G linear photodiode arrays 104a to 104c to the output units 310a to 310d in backward-direction reading. Horizontal CCD shift registers 306a to 306d transfer the charges generated in the G linear photodiode arrays 104a to 104d to output units 311a to 311d in forward-direction reading. Horizontal CCD shift registers 307a to 307d transfer the charges generated in the R linear photodiode arrays 102a to 102c to output units 308a to 308d in backward-direction reading.

**[0075]** Each of the horizontal CCD shift registers 304, 305, 306, and 307 has a four-line structure in which two lines extend in the horizontal direction, and each line is divided into two lines at the center. As for the transfer direction of the two CCD shift registers divided into two lines at the center, the reading direction is reversed from the center.

**[0076]** When the two horizontal CCD shift registers are arranged as in the color CCD linear image sensor in Fig. 8, the line-to-line distance between the linear photodiode arrays can be minimized (with four lines, the line-to-line distance is almost doubled). Additionally, when each line is divided into two lines, and the transfer direction is reversed, the same reading speed as in the four-line horizontal CCD shift registers which are horizontally arranged can be obtained (a reading speed twice that in the arrangement in Fig. 8 can be obtained).

**[0077]** The functions and operations of shift gates SH1 to SH8 (112 to 118 and 124), switch gates SG1 (119 and 125) and SG2 (120 and 126), transfer gates TG1 (121 and 127), TG2 (122 and 128), and TG3 (123 and 129) are the same as those in Fig. 8.

**[0078]** In the B and G CCD linear sensor units 302 and 303, the functions and operations of constituent elements with the same reference numerals or symbols as those in the R CCD linear sensor unit 301 are the same, and a detailed description thereof will be omitted.

**[0079]** The timing chart of this color CCD linear image sensor 300 is the same as that in Figs. 9A and 9B because it has the same arrangement as that of the color CCD linear image sensor 100 in Fig. 8 except for the structure of the horizontal CCD shift register. Therefore, the above description is referred to as a detailed description.

**[0080]** With the above arrangement, the number of division of the shift registers is increased without increasing the line-to-line distance between the photoelectric conversion means, thereby increasing the image reading speed.

**[0081]** A color linear image sensor is constituted by

forming a plurality of linear image sensors on a single wafer, each linear image sensor including a photoelectric conversion unit for converting light from an object into an electrical signal, a plurality of charge transfer units for transferring the signal converted into the electrical signal by the photoelectric conversion unit to an output unit, and color filters which are formed on the photoelectric conversion unit to color-separate the light from the object. A plurality of lines of photoelectric conversion units are arranged in each linear image sensor. At least one charge transfer unit is arranged between the photoelectric conversion units to synthesize output signals from the plurality of lines of photoelectric conversion units and to transfer the output signals from the photoelectric conversion units to the next photoelectric conversion unit. An accumulation unit temporarily stores the output signals from the photoelectric conversion units. By changing the method of applying a drive pulse, the charge transfer unit and the accumulation unit transfer the output signals in both forward and backward directions.

#### Claims

1. A linear image sensor for reading an original, comprising

a plurality of linear image sensors (101, 102, 103) each having a plurality of lines of photoelectric conversion means (102a-c, 103a-c, 104a-c) and being responsive to a light from an object which is converted into an electrical signal by the photoelectric conversion means; a plurality of first shift gates (SH1-6), arranged between the lines of photoelectric conversion means of said plurality of linear image sensors, for transferring charges from one line of the photoelectric conversion means to the next line of the photoelectric conversion means, thereby synthesizing the charges generated in the preceding lines of the photoelectric conversion means with the charges generated in the next line of the photoelectric conversion means; and color filters which are formed on the photoelectric conversion means to color-separate the light from the object;

#### characterized by

a plurality of pairs of shift register means (105a-b, 106a-b, 107a-b, 108a-b), arranged on each side of the linear image sensors, for transferring the electrical signal converted by the photoelectric conversion means to an output unit (109a-b, 110a-b, 111a-b, 130a-b); a plurality of second shift gates (SH7-8), arranged on each side of the linear image sen-

sors, wherein in the second shift gates an output signal is obtained which is multiplied by the number of lines of the photoelectric conversion means of the linear image sensors; a plurality of switch gates (SG1-2), arranged between the second shift gates and the pairs of shift register means, for transferring charges from the second shift gates to the pairs of shift register means; a plurality of transfer gates (TG1-3), arranged between the shift register means of said each pair of shift register means, for transferring charges between the shift register means of each pair of shift register means; and a signal terminal for changing a drive timing of a drive pulse for driving said first and second shift gates to switch a transfer direction of said first and second shift gates and said transfer gates between a forward direction and a backward direction.

2. A linear image sensor according to claim 1, **characterized in that** each shift register means of the plurality of pairs of shift register means is separated at a midpoint of the entire shift register, wherein a transfer direction of the shift register is reversed at the midpoint.
3. A linear image sensor according to claim 1, **characterized in that** said first shift gates have at least three lines.
4. A linear image sensor according to claim 1, **characterized in that** said transfer gates have at least three lines.
5. A linear image sensor according to claim 1, **characterized in that** the plurality of pairs of shift register means are arranged between the linear image sensors and are commonly used by the photoelectric conversion means located on both sides of the pairs of shift register means.
6. A linear image sensor according to claim 1, **characterized in that** the shift register means is a CCD shift register.

#### Patentansprüche

1. Zeilenförmiger Bildaufnahmesensor zum Lesen einer Vorlage, mit  
einer Vielzahl zeilenförmiger Bildaufnahmesensoren (101, 102, 103), die jeweils eine Vielzahl von Zeilen photoelektrischer Umwandlungseinrichtungen (102a-c, 103a-c, 104a-c)

aufweisen und auf durch die photoelektrischen Umwandlungseinrichtungen in ein elektrisches Signal umgewandeltes Licht von einem Gegenstand ansprechen,  
 einer Vielzahl zwischen den Zeilen der photoelektrischen Umwandlungseinrichtungen der Vielzahl der zeilenförmigen Bildaufnahmesensoren angeordneter erster Schiebegatter (SH1-6) zur Übertragung von Ladungen von einer Zeile der photoelektrischen Umwandlungseinrichtungen zur nächsten Zeile der photoelektrischen Umwandlungseinrichtungen, wodurch die in den vorher gehenden Zeilen der photoelektrischen Umwandlungseinrichtungen erzeugten Ladungen mit den in der nächsten Zeile der photoelektrischen Umwandlungseinrichtungen erzeugten Ladungen, zusammengefügt werden, und  
 Farbfiltern, die auf den photoelektrischen Umwandlungseinrichtungen gebildet sind, um das Licht von dem Gegenstand farblich zu trennen,

#### gekennzeichnet durch

eine Vielzahl von auf jeder Seite der zeilenförmigen Bildaufnahmesensoren angeordneten Schieberegistereinrichtungspaaren (105a-b, 106a-b, 107a-b, 108a-b) zur Übertragung des durch die photoelektrischen Umwandlungseinrichtungen umgewandelten elektrischen Signals zu einer Ausgabereinheit (109a-b, 110a-b, 111a-b, 130a-b),  
 eine Vielzahl von auf jeder Seite der zeilenförmigen Bildaufnahmesensoren angeordneten zweiten Schiebegatter (SH7-8), wobei in den zweiten Schiebegattern ein mit der Anzahl der Zeilen der photoelektrischen Umwandlungseinrichtungen der zeilenförmigen Bildaufnahmesensoren multipliziertes Ausgangssignal erhalten wird,  
 eine Vielzahl von zwischen den zweiten Schiebegattern und den Schieberegistereinrichtungspaaren angeordneten Schaltgattern (SG1-2) zur Übertragung von Ladungen von den zweiten Schiebegattern zu den Schieberegistereinrichtungspaaren,  
 eine Vielzahl von zwischen den Schieberegistereinrichtungen jedes Schieberegistereinrichtungspaares angeordneten Übertragungsgattern (TG1-3) zur Übertragung von Ladungen zwischen den Schieberegistereinrichtungen jedes Schieberegistereinrichtungspaares, und  
 einen Signalanschluß zur Veränderung des Ansteuerzeitverlaufs eines Ansteuerimpulses zur Ansteuerung der ersten und zweiten Schiebegatter, um die Übertragungsrichtung der ersten und zweiten Schiebegatter und der Übertra-

gungsgatter zwischen einer Vorwärts- und einer Rückwärtsrichtung umzuschalten.

2. Zeilenförmiger Bildaufnahmesensor nach Anspruch 1,  
**dadurch gekennzeichnet, daß**  
 jede Schieberegistereinrichtung der Vielzahl der Schieberegistereinrichtungspaare am Mittelpunkt des gesamten Schieberegisters geteilt ist, wobei die Übertragungsrichtung des Schieberegisters am Mittelpunkt umgekehrt wird.
3. Zeilenförmiger Bildaufnahmesensor nach Anspruch 1,  
**dadurch gekennzeichnet, daß**  
 die ersten Schiebegatter zumindest drei Zeilen aufweisen.
4. Zeilenförmiger Bildaufnahmesensor nach Anspruch 1,  
**dadurch gekennzeichnet, daß**  
 die Übertragungsgatter zumindest drei Zeilen aufweisen.
5. Zeilenförmiger Bildaufnahmesensor nach Anspruch 1,  
**dadurch gekennzeichnet, daß**  
 die Vielzahl der Schieberegistereinrichtungspaare zwischen den zeilenförmigen Bildaufnahmesensoren angeordnet sind und von den auf beiden Seiten der Schieberegistereinrichtungspaare gelegenen photoelektrischen Umwandlungseinrichtungen gemeinsam verwendet werden.
6. Zeilenförmiger Bildaufnahmesensor nach Anspruch 1,  
**dadurch gekennzeichnet, daß**  
 die Schieberegistereinrichtung ein CCD-Schieberegister darstellt.

#### Revendications

1. Détecteur linéaire d'image pour lire un original, comprenant  
 une pluralité de détecteurs linéaires (101, 102, 103) d'image comportant chacun une pluralité de lignes de moyens (102a-c, 103a-c, 104a-c) de conversion photoélectrique et étant sensibles à une lumière en provenance d'un objet, qui est convertie en un signal électrique par le moyen de conversion photoélectrique ;  
 une pluralité de premières portes (SH1-6) à décalage, disposées entre les lignes de moyens de conversion photoélectrique de ladite pluralité de détecteurs linéaires d'image, pour transférer des charges entre une ligne du moyen de



conversion photoélectrique et la ligne suivante du moyen de conversion photoélectrique, afin de synthétiser les charges générées dans les lignes précédentes du moyen de conversion photoélectrique avec les charges générées dans la ligne suivante du moyen de conversion photoélectrique ; et

des filtres de couleurs qui sont formés sur le moyen de conversion photoélectrique pour séparer en couleurs la lumière en provenance de l'objet ;

#### caractérisé par

une pluralité de paires de moyens (105a-b, 106a-b, 107a-b, 108a-b) formant registres à décalage, disposées de chaque côté des détecteurs linéaires d'image, pour transférer le signal électrique converti par le moyen de conversion photoélectrique vers une unité de sortie (109a-b, 110a-b, 111a-b, 130a-b) ;

une pluralité de deuxièmes portes (SH7-8) à décalage, disposées de chaque côté des détecteurs linéaires d'image, où dans les deuxièmes portes à décalage est obtenu un signal de sortie, qui est multiplié par le nombre de lignes du moyen de conversion photoélectrique des détecteurs linéaires d'image ;

une pluralité de portes (SG1-2) de commutation, disposées entre les deuxièmes portes à décalage et les paires de moyens formant registres à décalage, pour transférer des charges entre les deuxièmes portes à décalage et les paires de moyens formant registres à décalage ;

une pluralité de portes de transfert (TG1-3), disposées entre le moyen formant registre à décalage de ladite chaque paire de moyens formant registres à décalage, pour transférer des charges entre le moyen formant registre à décalage de chaque paire de moyens formant registres à décalage ; et

une borne de signal pour modifier une synchronisation d'attaque d'une impulsion d'attaque pour attaquer lesdites première et deuxièmes portes à décalage pour commuter une direction de transfert desdites première et deuxième portes à décalage et desdites portes de transfert entre une direction vers l'avant et une direction vers l'arrière.

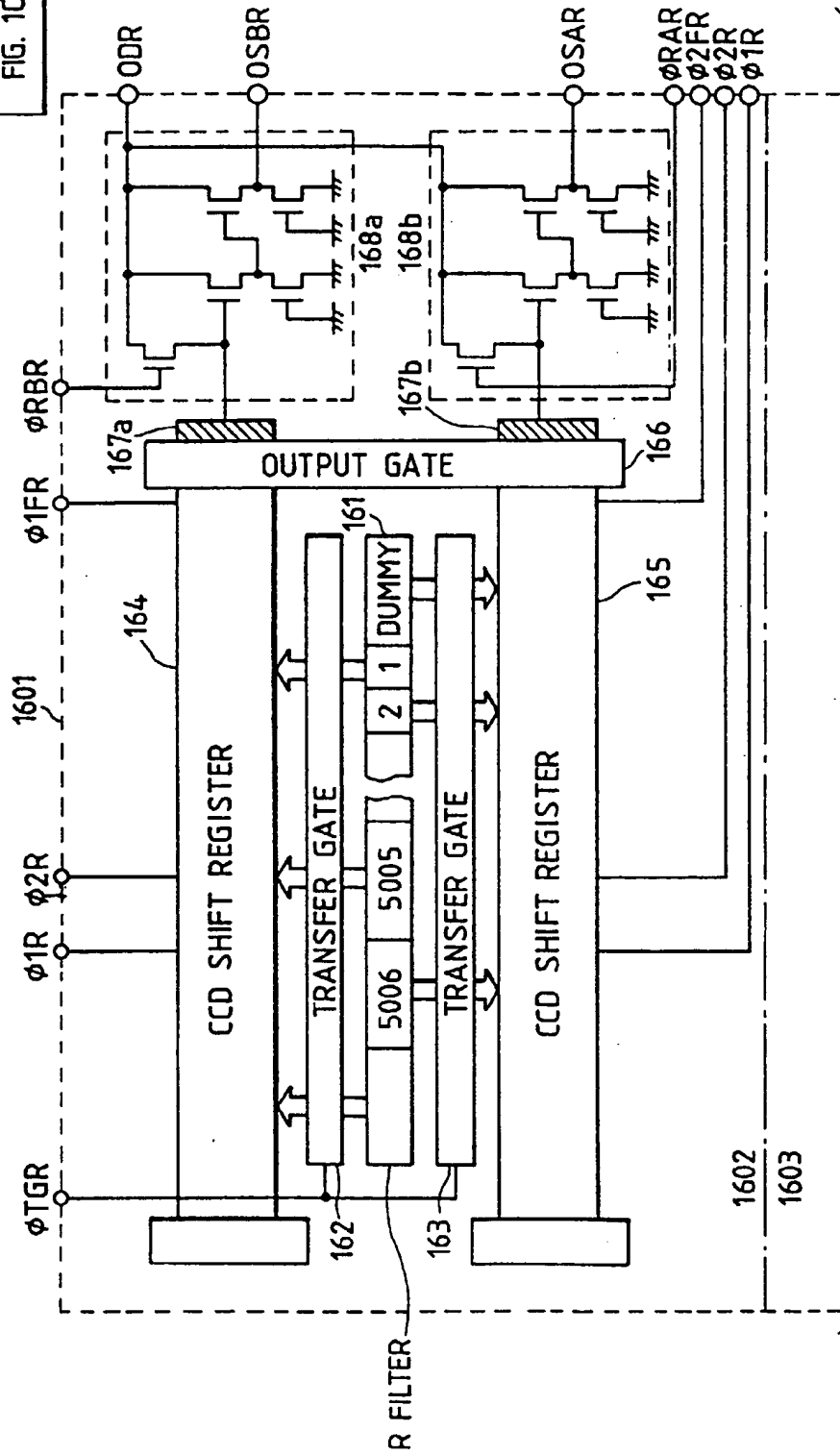
2. Détecteur linéaire d'image selon la revendication 1, **caractérisé en ce que** chaque moyen formant registre à décalage de la pluralité de paires de moyens formant registres à décalage est séparé en un point milieu de la totalité du registre à décalage, une direction de transfert du registre à décalage étant inversée au point mi-

lieu.

3. Détecteur linéaire d'image selon la revendication 1, **caractérisé en ce que** lesdites premières portes à décalage ont au moins trois lignes.
4. Détecteur linéaire d'image selon la revendication 1, **caractérisé en ce que** lesdites portes de transfert ont au moins trois lignes.
5. Détecteur linéaire d'image selon la revendication 1, **caractérisé en ce que** la pluralité de paires de moyens formant registres à décalage sont disposées entre les détecteurs linéaires d'image et sont utilisées en commun par le moyen de conversion photoélectrique situé des deux côtés des paires de moyens formant registres à décalage.
6. Détecteur linéaire d'image selon la revendication 1, **caractérisé en ce que** le moyen formant registre à décalage est un registre à décalage CCD.

FIG. 1A
FIG. 1B
FIG. 1C

FIG. 1A  
PRIOR ART



TO FIG. 1B

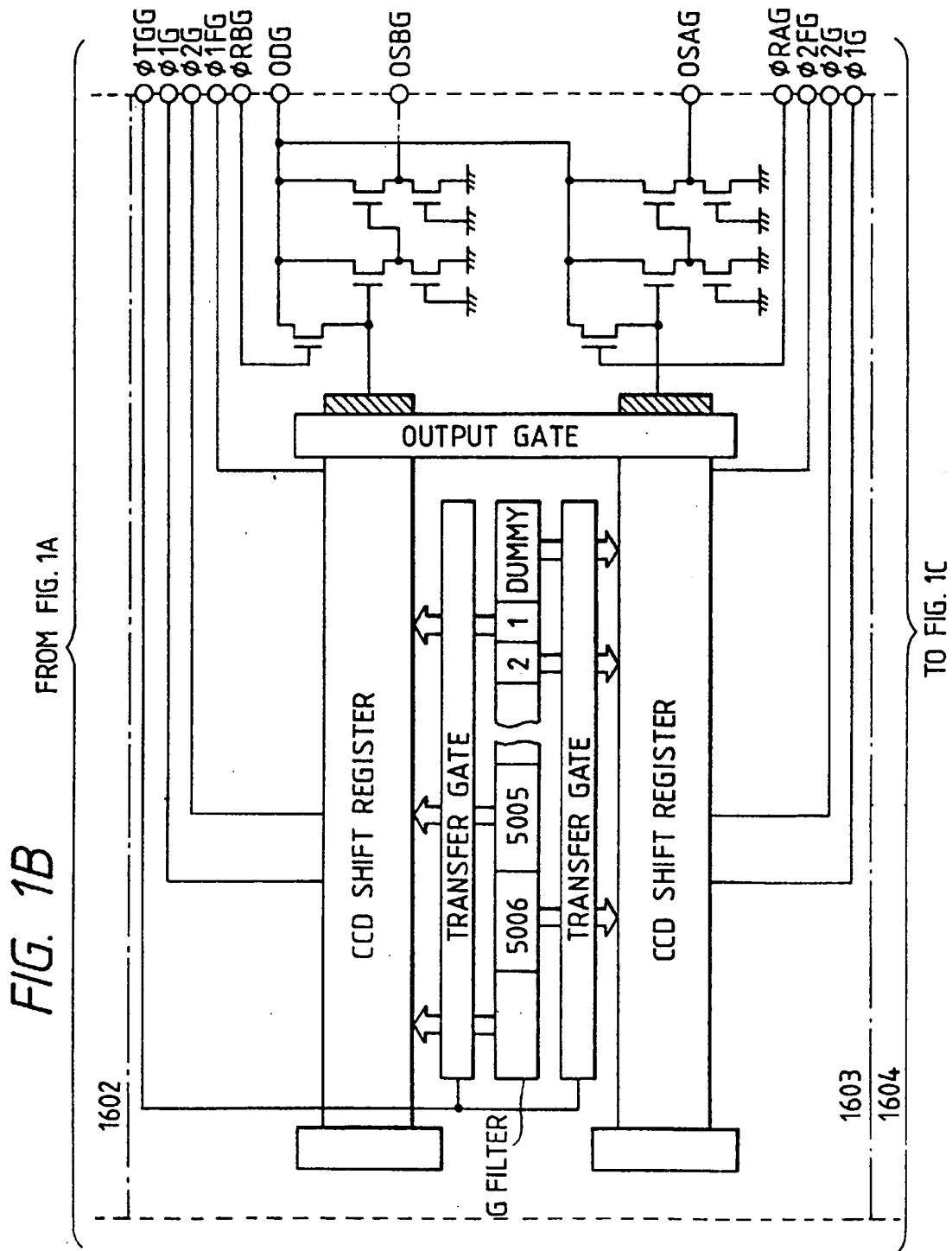


FIG. 1C

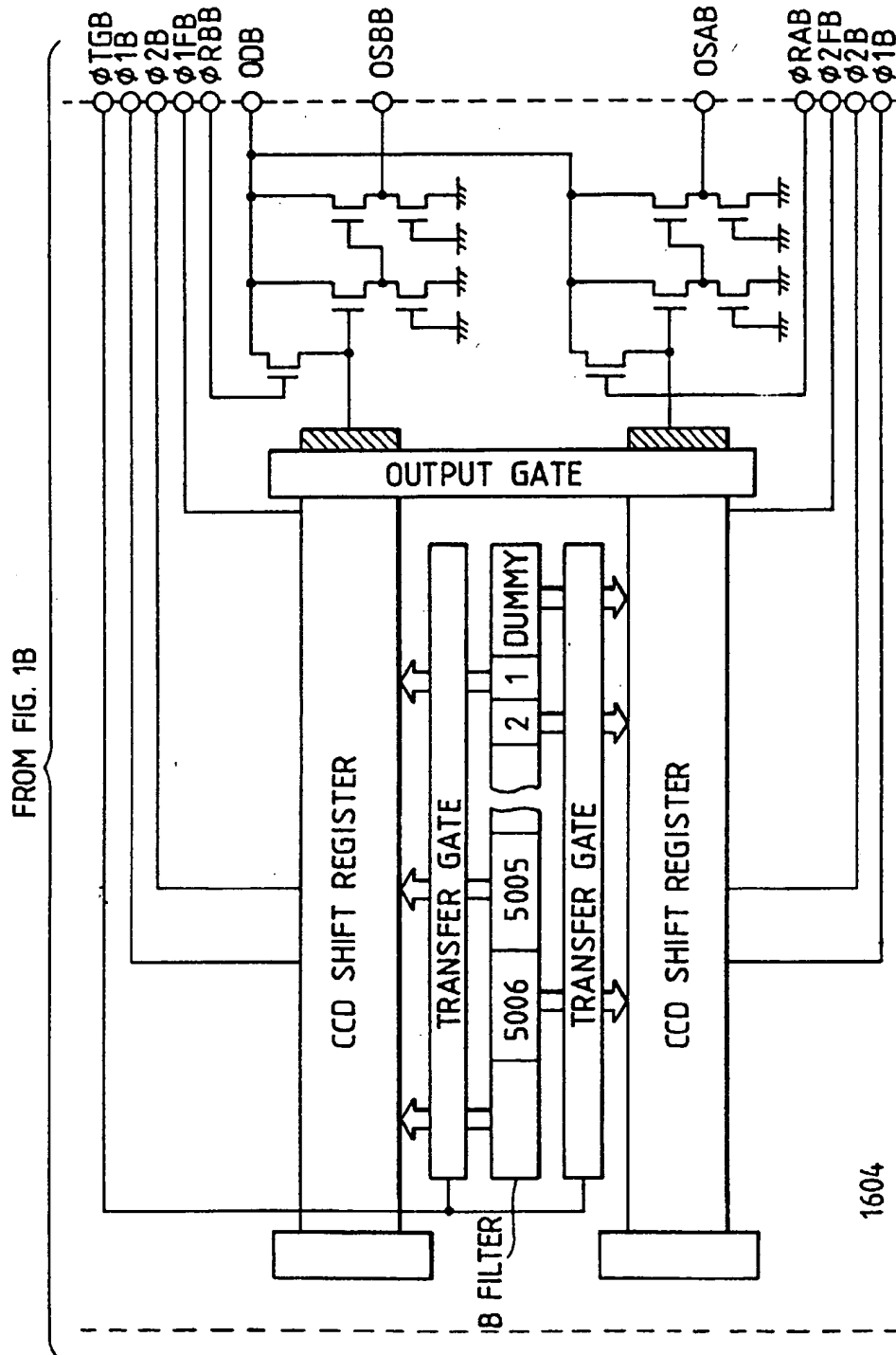


FIG. 2

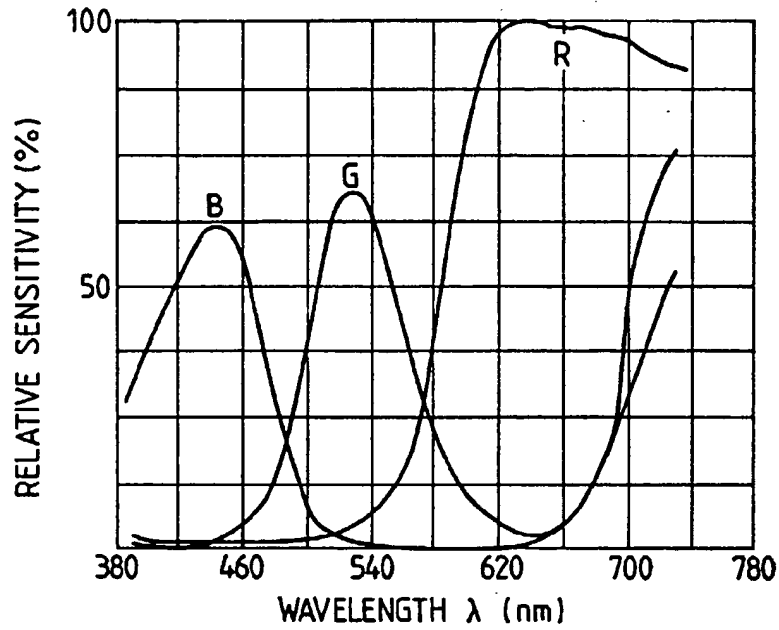


FIG. 3

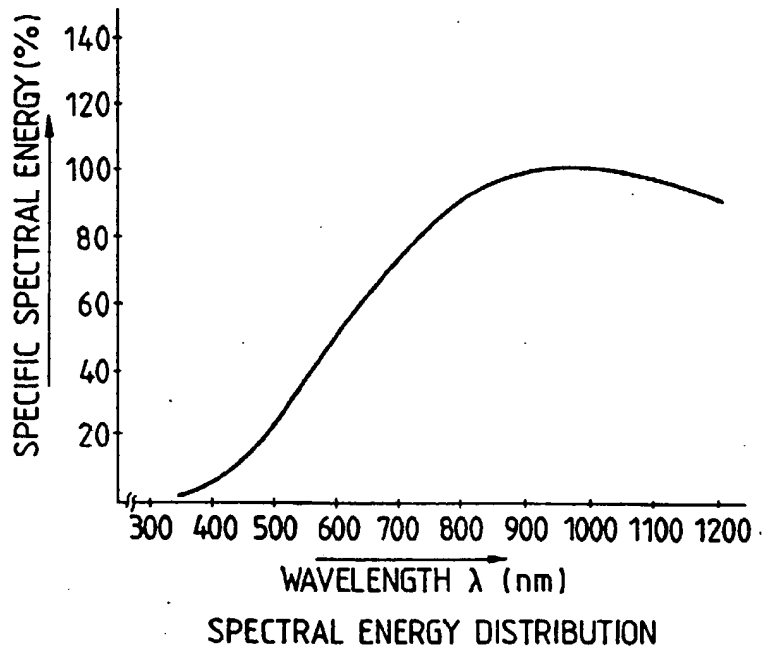


FIG. 4

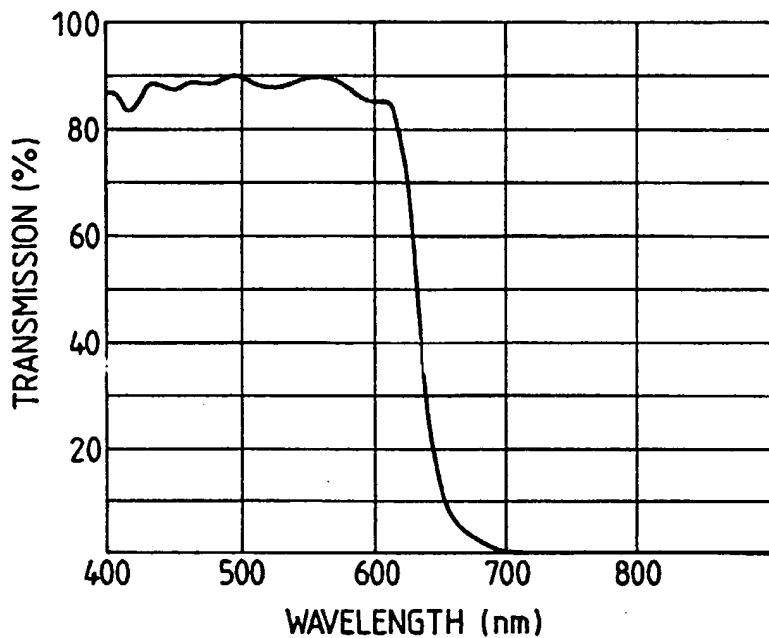


FIG. 5

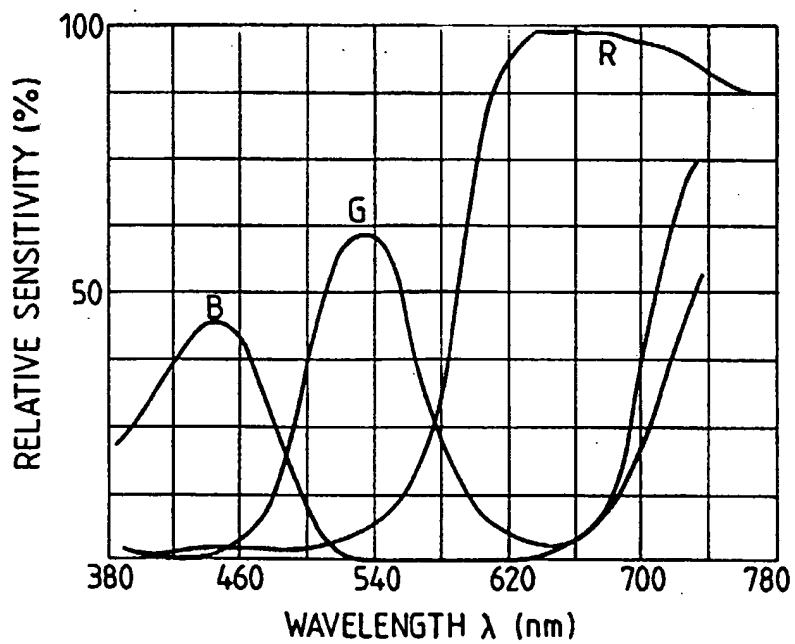


FIG. 6

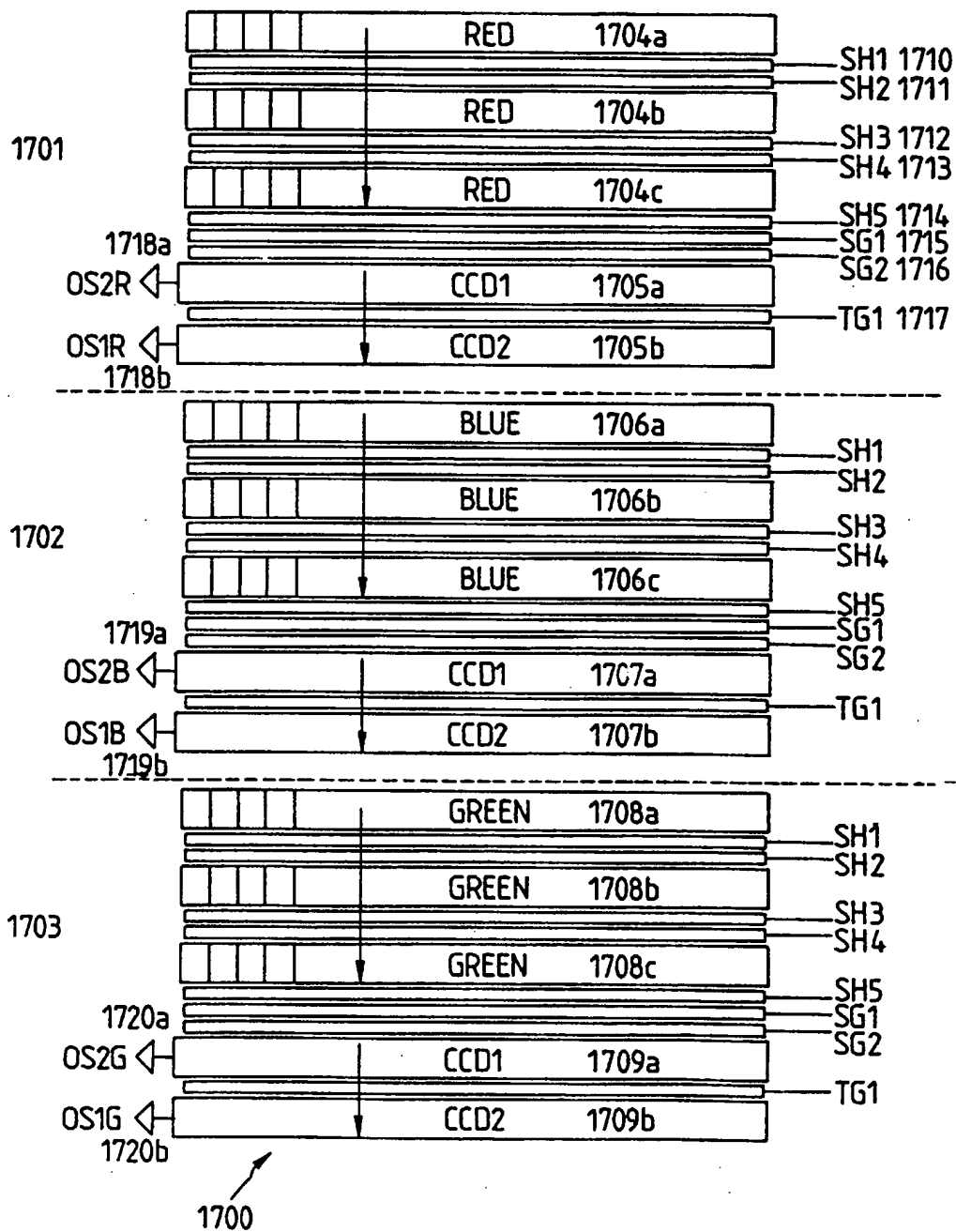


FIG. 7

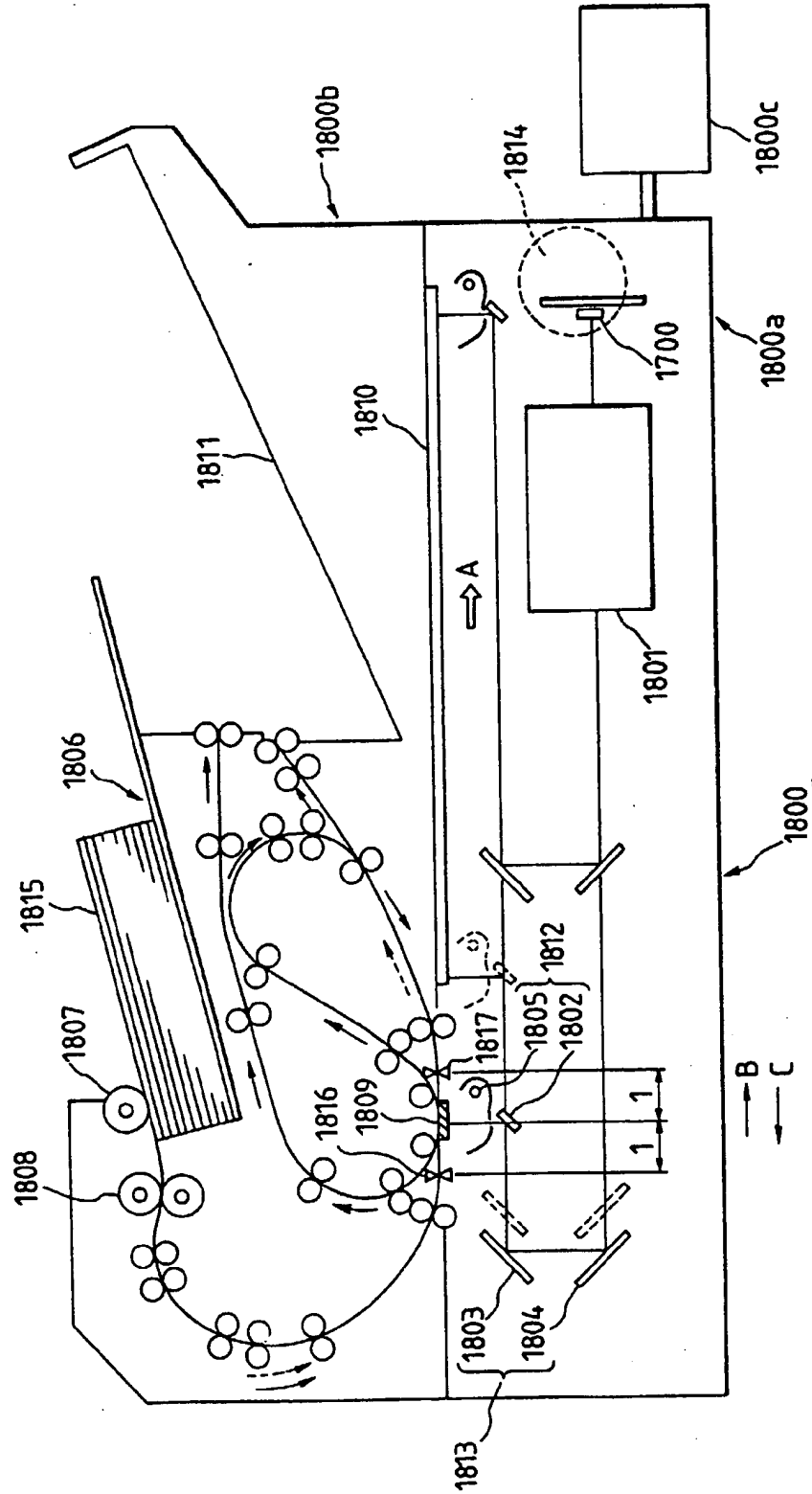




FIG. 8

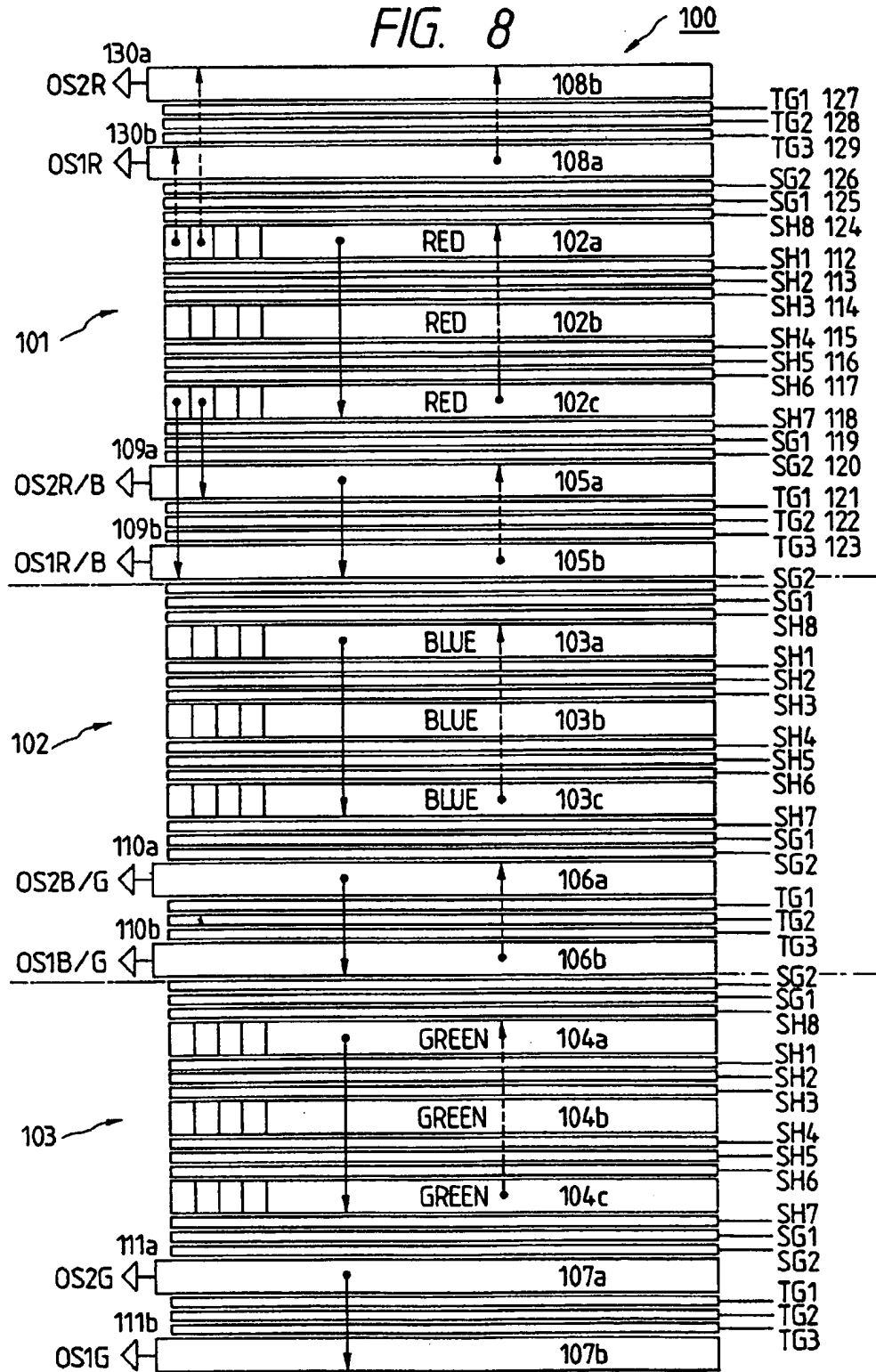


FIG. 9A

FORWARD-DIRECTION READING

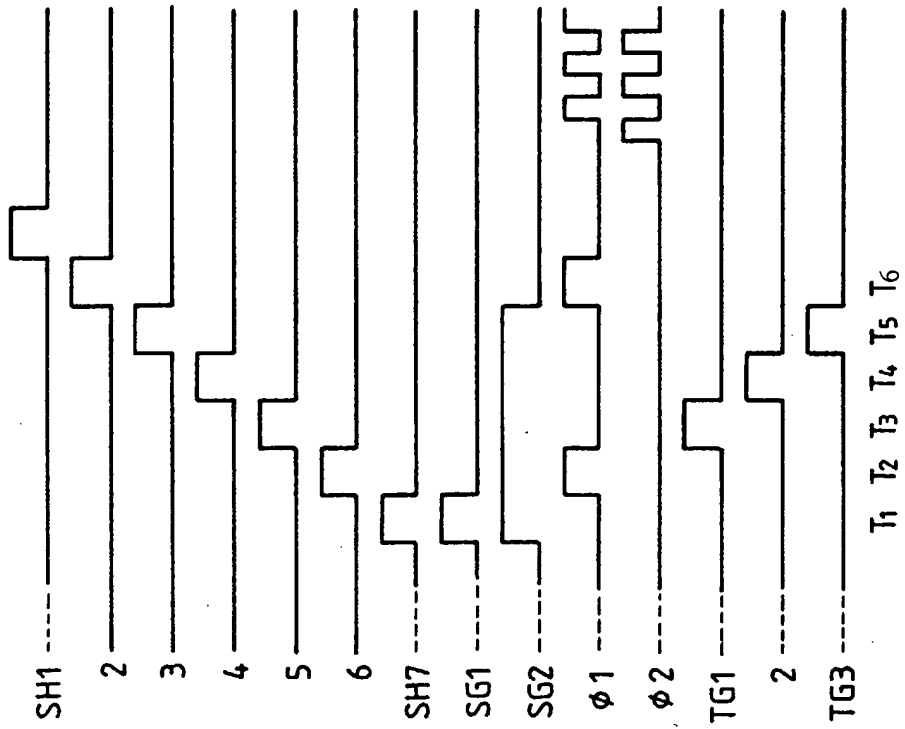


FIG. 9B

BACKWARD-DIRECTION READING

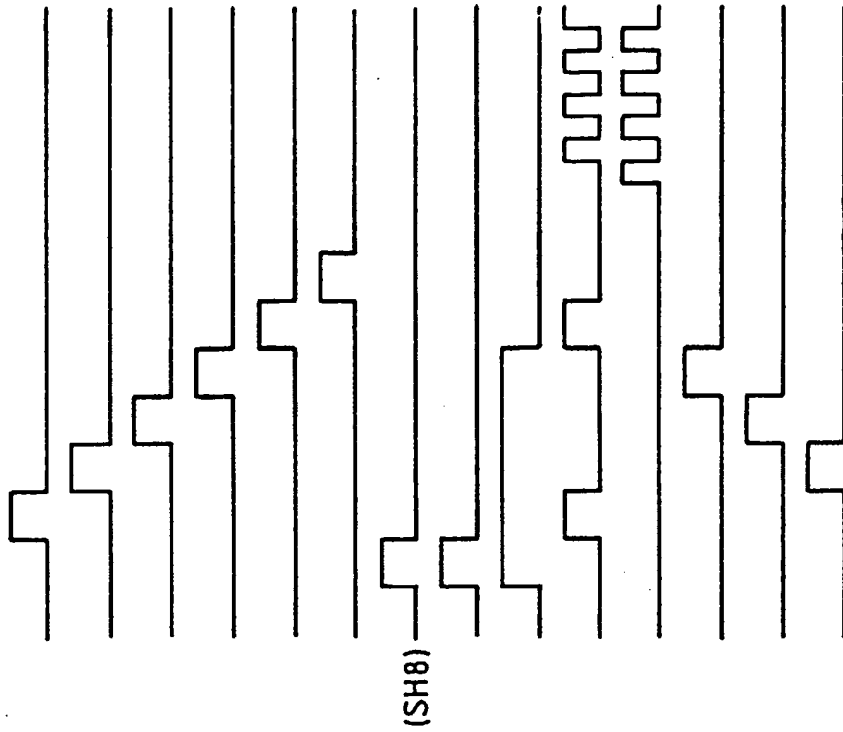


FIG. 10

